



P21535.A06

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Yoshitaka SUMIDA

Group Art Unit: 2181

Appln. No. : 10/000,132

Examiner: A. ELAMIN

Filed : December 4, 2001

Confirmation No.: 8473

For : METHOD FOR REDUCING POWER CONSUMPTION OF CPU,  
ELECTRONIC APPARATUS, AND RECORDING MEDIUM HAVING  
POWER CONSUMPTION REDUCTION PROGRAM RECORDED  
HEREON

**COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE**

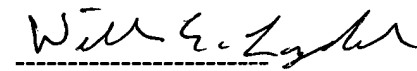
Commissioner for Patents  
U.S. Patent and Trademark Office  
Customer Service Window, Mail Stop Issue Fee  
Randolph Building  
401 Dulany Street  
Alexandria VA 22314

Sir:

In response to the Notice of Allowability, mailed by the Patent and Trademark Office on November 26, 2004, and to the Statement of Reasons for Allowance attached thereto, Applicant wishes to clarify the record with respect to the basis for the patentability of claims in the present application. In this regard, while Applicant does not disagree with the Examiner's indication that certain identified features are not disclosed by the references, Applicant submits that each of the claims in the present application recite a particular combination of features, and that the basis for patentability of each of these claims is based on the totality of the particular features recited therein.

Should there be any questions, the Examiner is invited to contact the undersigned at the below-listed telephone number.

Respectfully submitted,  
Yoshitaka SUMIDA

  
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February 25, 2005  
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